



i8085 opcode chart

LSB =>	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	NOP	LXI	STAX	INX	INR	DCR	MVI	RLC	-	DAD	LDAX	DCX	INR	DCR	MVI	RRC
1	-	LXI	STAX	INX	INR	DCR	MVI	RAL	-	DAD	LDAX	DCX	INR	DCR	MVI	RAR
2	RIM	LXI	SHLD	INX	INR	DCR	MVI	DAA	-	DAD	LHLD	DCX	INR	DCR	MVI	CMA
3	SIM	LXI	STA	INX	INR	DCR	MVI	STC	-	DAD	LDA	DCX	INR	DCR	MVI	CMC
4	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV
5	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV
6	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV
7	MOV	MOV	MOV	MOV	MOV	MOV	HLT	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV	MOV
8	ADD	ADD	ADD	ADD	ADD	ADD	ADD	ADD	ADC	ADC	ADC	ADC	ADC	ADC	ADC	ADC
9	SUB	SUB	SUB	SUB	SUB	SUB	SUB	SUB	SBB	SBB	SBB	SBB	SBB	SBB	SBB	SBB
A	ANA	ANA	ANA	ANA	ANA	ANA	ANA	ANA	XRA	XRA	XRA	XRA	XRA	XRA	XRA	XRA
B	ORA	ORA	ORA	ORA	ORA	ORA	ORA	ORA	CMP	CMP	CMP	CMP	CMP	CMP	CMP	CMP
C	RNZ	POP	JNZ	JMP	CNZ	PUSH	ADI	RST	RZ	RET	JZ	-	CZ	CALL	ACI	RST
D	RNC	POP	JNC	OUT	CNC	PUSH	SUI	RST	RC	-	JC	IN	CC	-	SBI	RST
E	RPO	POP	JPO	XTHL	CPO	PUSH	ANI	RST	RPE	PCHL	JPE	XCHG	CPE	-	XRI	RST
F	RP	POP	JP	DI	CP	PUSH	ORI	RST	RM	SPHL	JM	EI	CM	-	CPI	RST

 z80 incompatible
 GB incompatible